PATENT AND TRADEMARK OFFICE IN THE UNITED STATES

In repatent application of

Noboru Morita, et al.

Serial No.:

10/766,921

Group Art Unit:

Not Yet Assigned

Filing Date: January 30, 2004

Examiner:

Unknown

For:

METHOD OF FABRICATING SEMICONDUCTOR DEVICE USING PLASMA-

ENHANCED CVD

Commissioner for Patents Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following document listed on the attached form PTO-1449, to the attention of the Examiner in charge of the aboveidentified application. A copy of the listed document is provided herewith for the convenience of the Examiner.

This citation does not constitute an admission that the reference is relevant or material to the claims. It is only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the listed reference be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,

Sean M. McGinn, Esq.

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Form PTO-A820 (also form PTO-1449) P09A/REV04

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